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LightTrader : World's first AI-enabled High-Frequency Trading Solution with 16 TFLOPS / 64 TOPS Deep Learning Inference Accelerators

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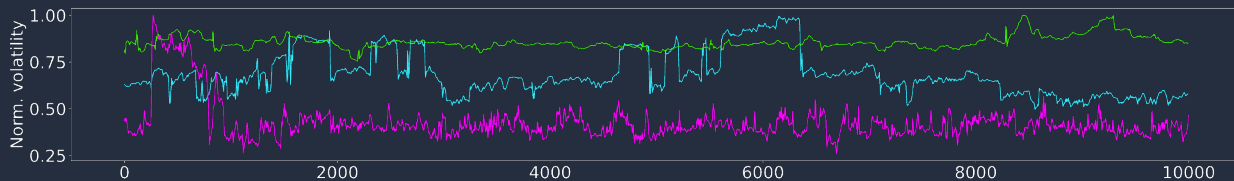
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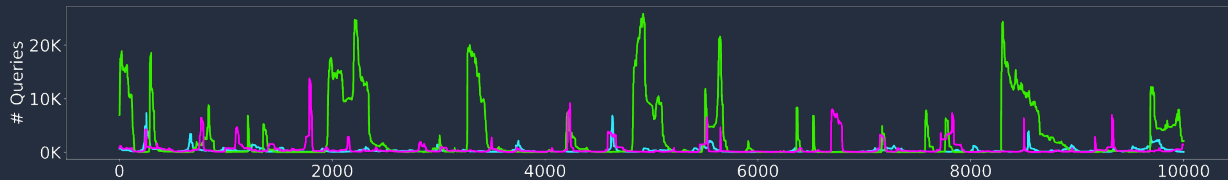


We present the **world's first AI-enabled high-frequency trading (HFT) system, LightTrader**, which integrates the custom AI accelerators and the FPGA-based conventional HFT pipeline for the low-latency-high-throughput trading solutions with a reduced query miss rate. For better utilization, adaptive job scheduling methods are also proposed to further improve the performance, where layer-wise workload scaling and dynamic voltage-frequency scaling (DVFS) techniques progressively adjust the workloads of AI accelerators, in conjunction with the architecture support. LightTrader integrating TSMC 7nm tape-out accelerators solely achieves **6x speed-up of DNN processing** and **30-50x reduction of query miss rate** without the scheduling method while the scheduling scheme further improves the **energy efficiency by 25%** and reduces the **query miss rate by 2.4x**.

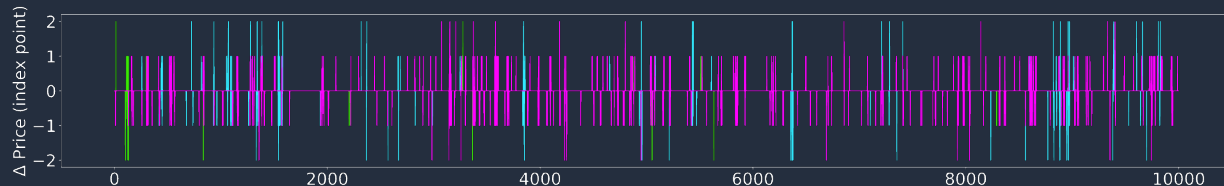
HFT craves leveraging AI algorithms with breakthrough technologies of realizing low-latency-high-throughput, under the extremely strict constraints



Complicated market
data patterns



Bursty input query
for AI processing

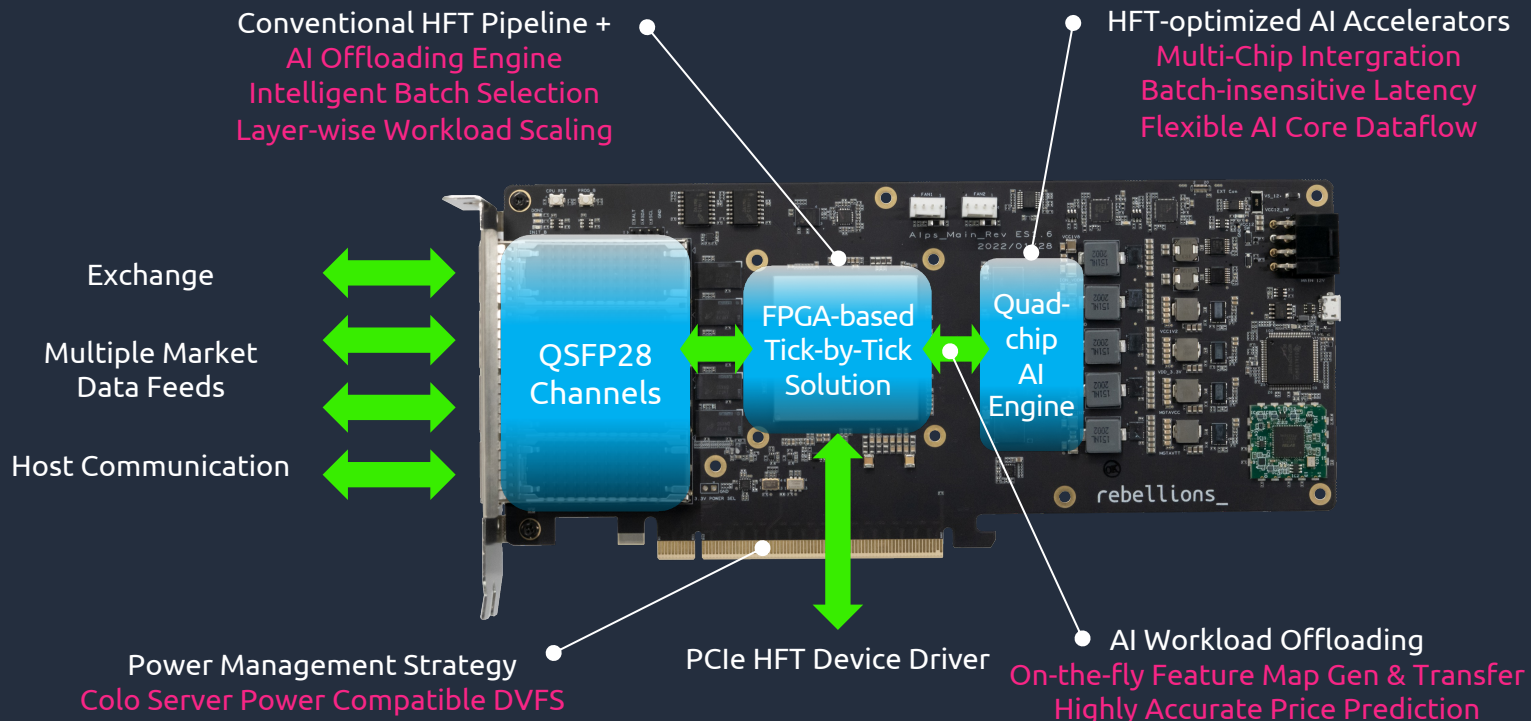


Unpredictable
profit opportunities

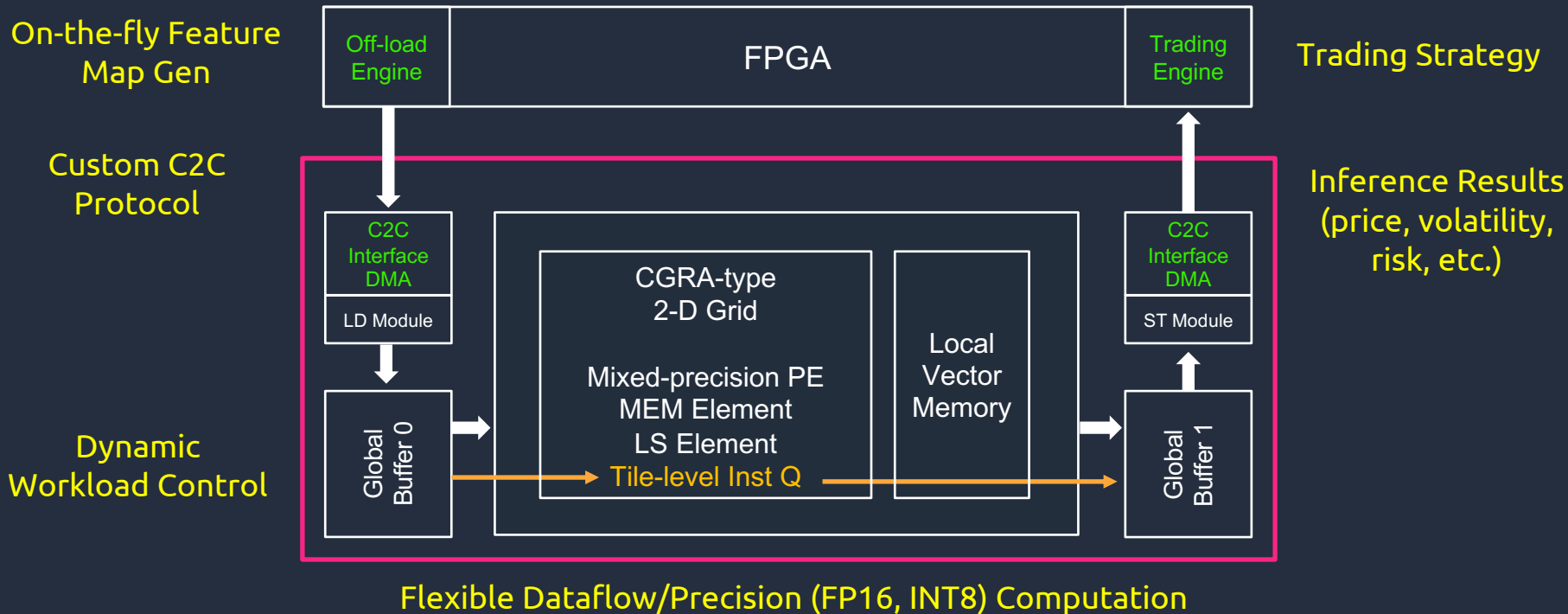
— E-mini S&P 500 Futures — E-mini Crude Oil Futures — Ultra U.S. Treasury Bond Futures

LightTrader can exploit the superb performance of AI for HFT strategies, which have been impossible for conventional systems

Heterogeneous HFT system on a board : FPGA + AI accelerators

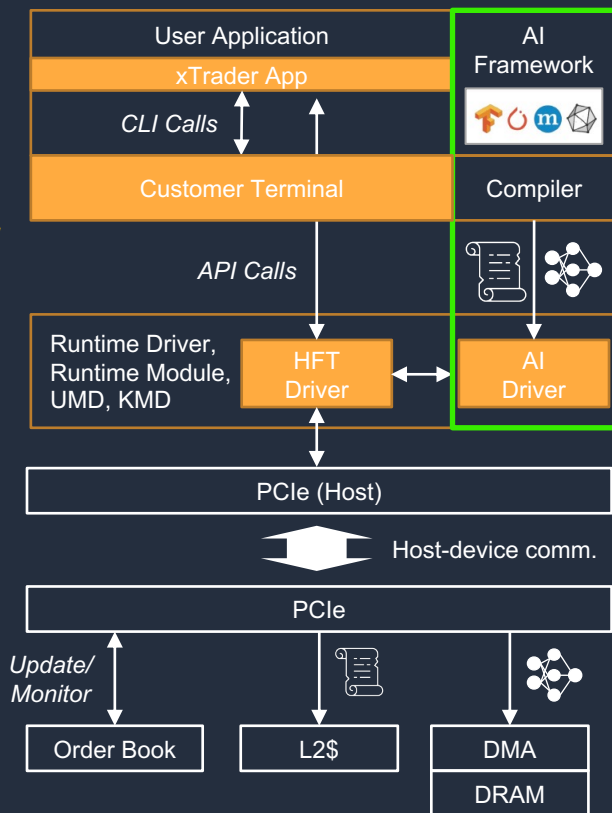


Low-latency companion AI accelerators : 4 TFLOPS / 16 TOPS from single chip



AI-enabled HFT software stack with User API

Low-latency
HFT Stack



HFT-optimized
AI Stack

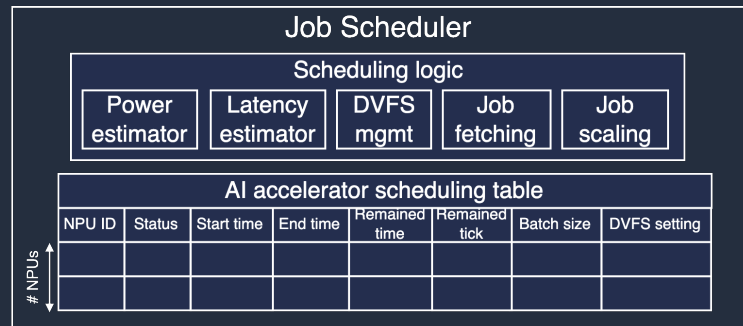
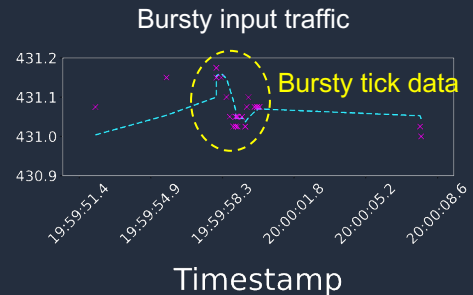
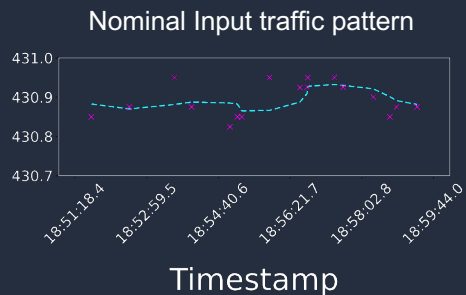
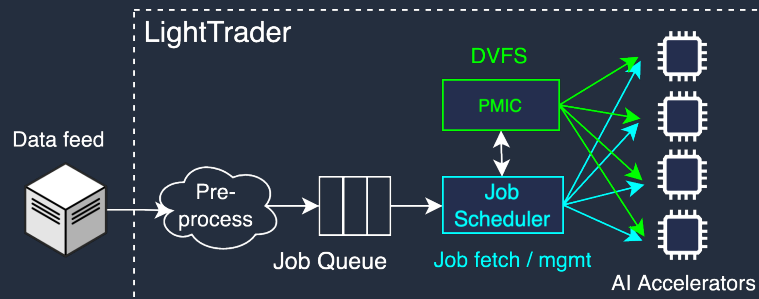
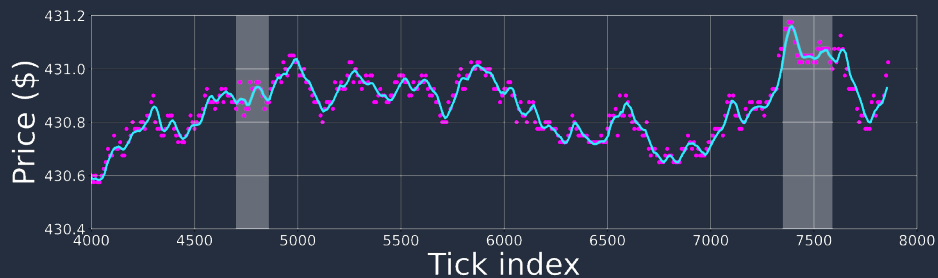
LightTrader

The HFT software stack provides an end-to-end solution for the low-latency finance AI processing on the LightTrader hardware

- The software stack includes
- Compiler
 - DL Inference driver
 - HFT pipeline driver
 - User application interface

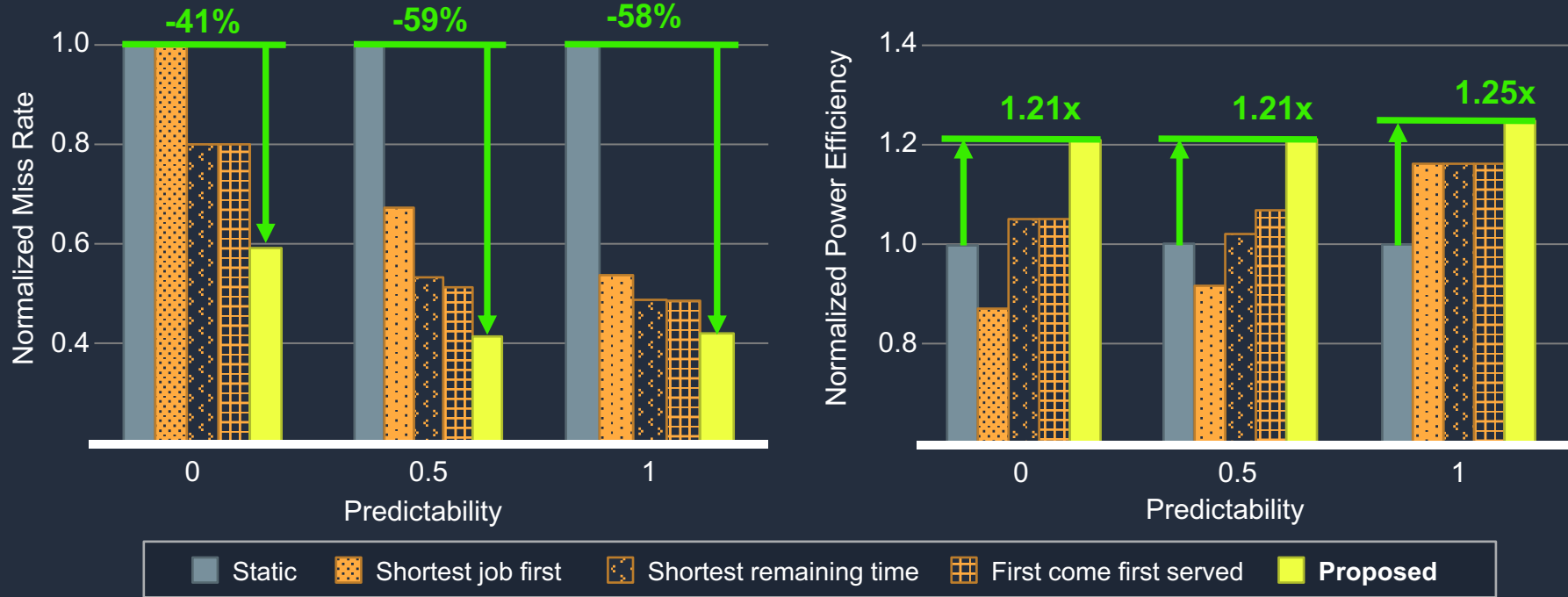
The PCIe Gen 4.0 x16 interface enables a real-time hardware control and monitor

Job Scheduling enabled by HW features for optimizing throughput



The advanced scheduling features retain high throughput for tick-by-tick inference jobs even for the bursting input query

Miss rate and power efficiency improvements from the intelligent scheduling



The advanced scheduling minimizes the probability of missing input queries

Prototype system integration for AI-based HFT compute node



- Integrating eight boards into a standard 4U rack form-factor, the proposed server-level solution extends the capability of the LightTraders up to
- **128 TFLOPS / 512 TOPS**
 - **3.2 Tbps** query processing throughput
 - **10~100 us** DL inference-based tick-to-trade latency
 - **only with 35x8 W** board power

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Revolutionize AI Silicon

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