AMD

MLIR Tutorial

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Fellow

August 21, 2022

Context

End of Growth of Single Program Speed? 40 years of Processor Performance 100000 End of 10000 the line? 2X/20 yrs (3%/yr) Amdahl's 1000 End of Law → Dennard 2X/6 yrs Scaling 100 (12%/yr) + Multicore 2X/3.5 yrs RISC CISC 10 2X/1.5 yrs 2X/3.5 vrs (52%/yr) (22%/yr 1980 1985 1990 1995 2000 2005 2010 2015

Technology & Power: Dennard Scaling



A New Golden Age for Computer Architecture A New Golden Age for Compilers

Performance vs. VAX11-780

MLIR: Multi-Level Intermediate Representation



Next generation open source compiler infrastructure

LLVM core project

Well positioned to support this new golden age!





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MLIR Basics	Jacques Pienaar (Google)
CPU and GPU Code Generation	Harsh Menon (Nod.AI)
Break	
ML Frontends and TOSA	Suraj Sudhir (ARM)
Hardware Design and CIRCT	Andrew Lenhart (SiFive) and John Demme (Microsoft)

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